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September 11, 2003

MAIL STOP PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

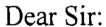
Re: Application of Motoaki MATSUDA

FLIP-CHIP BGA SEMICONDUCTOR DEVICE FOR ACHIEVING A

SUPERIOR CLEANING EFFECT

Assignee: NEC ELECTRONICS CORPORATION

Our Ref. Q77402



Attached hereto is the application identified above comprising 12 sheets of the specification, including the claims and abstract, 6 sheets of drawings, executed Assignment and PTO 1595 form, and executed Declaration and Power of Attorney. Also enclosed is an Information Disclosure Statement and PTO/SB/08 A & B (modified) with references.

The Government filing fee is calculated as follows:

TOTAL FILING FEE						\$750.00
Total claims Independent claims Base Fee	<u>6</u> - 20 <u>1</u> - 3	=	_ x _ x	\$18.00 \$84.00	=	\$.00 \$.00 \$750.00

Recordation of Assignment

TOTAL FEE

\$40.00

\$790.00

Checks for the statutory filing fee of \$750.00 and Assignment recordation fee of \$40.00 are attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.





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Priority is claimed from:

Country

Application No

Filing Date

Japan

2002-266695

September 12, 2002

The priority document is enclosed herewith.

Respectfully submitted, SUGHRUE MION, PLLC

Attorneys for Applicant

Bv·

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WASHINGTON OFFICE

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